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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10037039	FILING DATE 12/21/2001	CLASS 281 41	SUBCLASS 155	GAU 281 281	EXAMINER L. Mai
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**APPLICANTS: Breinlinger Richard;

**CONTINUING DATA VERIFIED: NO LM

** FOREIGN APPLICATIONS VERIFIED: NO LM

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		SAA-83
Verified and Acknowledged Examiners's initials LM		

TITLE : Low cost multi-range input circuit for industrial analog input modules
U.S. DEPT. OF COMM. (PAT. & TM.) PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for 0.6
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
			Print Fig. 3
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE	
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